

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Om P. Agrawal; Bai Nguyen; Kuang Chi; Brad Sharpe-Geisler; Giap Tran
Assignee: Lattice Semiconductor Corporation
Title: Scalable Serializer-Deserializer Architecture And Programmable Interface
Serial No.: Unassigned Filing Date: Herewith
Examiner: Unknown Group Art Unit: Unknown
Docket No.: M-15135 US

Irvine, California
July 14, 2003

COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §1.97 AND §1.98**

Dear Sir:

Pursuant to 37 C.F.R. §1.97 and §1.98, Applicant calls the following documents (copies enclosed) to the attention of the Examiner. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom. Note that U.S. patent application no. 10/023,226 is being submitted for the Examiner to consider regarding double patenting.

1. A PTO form 1449 listing these documents is enclosed.

Citation of the above documents shall not be construed as:

2. An admission that the documents are necessarily prior art with respect to the instant invention;
3. A representation that a search has been made, or
4. An Admission that the information cited herein is, or is considered to be material to patentability as defined in §1.56(b).

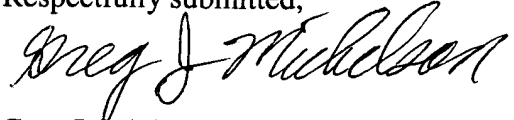
Applicant believes that no fee is required for submission of this statement. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's

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Deposit Account No. 50-2257. Please deduct any additional fees from, or credit any overpayment to the above-noted Deposit Account.

Express Mail #EV252519715US

Respectfully submitted,



Greg J. Michelson
Attorney for Applicants
Reg. No. 44,940

U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Serial No.		
				M-15135 US		Not yet assigned		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s)				
(Use several sheets if necessary)				Om P. Agrawal et al.				
				Filing Date		Group		
				Herewith		Not yet assigned		
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	5,877,632	03/02/1999	Goetting et al.				
	AB	6,031,428	02/29/2000	Hill				
	AC	6,218,858	04/17/2001	Menon et al.				
	AD	6,417,692	07/09/2002	Shiflet				
	AE	6,480,026	11/12/2002	Andrews et al.				
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL	WO 01/69837	03/14/2001	PCT				
	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ	"Converting Wide, Parallel Data Buses to High Speed Serial Links," by Jason Konstas, International IC '99, Conference Proceedings, pages 19-30						
	AR	Patent application 10/023,226 to Am P. Agrawal et al., filed 12/14/2001, with Preliminary Amendment						
	AS							
Examiner			Date Considered					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								